

<b>Integration</b>						
<b>Module Code</b>	<b>Workload</b>	<b>Credits</b>	<b>Semester</b>	<b>Frequency of Module</b>	<b>Duration</b>	
	180 hrs.	6	1	Only winter semester	1 Semester	
<b>1</b>	<b>Module Components</b>		<b>Teaching Language</b>	<b>Contact Hours</b>	<b>Self Study</b>	<b>Class Size</b>
	a) Technology of $\mu$ Systems + $\mu$ Electronics		a) English	a) 67,5 hrs.	a) 112,5 hrs.	a) 24
<b>2</b>	<p><b>Learning Outcomes</b></p> <p>After successful completion of the module the students</p> <p><b>Knowledge (1)</b></p> <ul style="list-style-type: none"> <li>... should be able to list the most important lithography techniques</li> <li>... should be able to name several thin film deposition techniques</li> <li>... should be able to present content of lab work to other students</li> <li>... should be aware of main steps for fabrication of miniaturized systems</li> <li>... should identify etching techniques for defined structures</li> <li>... should know clean basic room concepts and clean room quality parameters</li> </ul> <p><b>Comprehension (2)</b></p> <ul style="list-style-type: none"> <li>... are able to understand the difference between hybrid and monolithical integration</li> <li>... are able to describe process details of processes made in the lab to the lecturer and other group members</li> <li>... are able to select proper lithography parameters for given structure size</li> </ul> <p><b>Application (3)</b></p> <ul style="list-style-type: none"> <li>... are able to perform specific process steps by themselves</li> <li>... are able to calculate process time for thermal oxidation</li> <li>... are able to calculate resolution of a given lithography process</li> <li>... are able to construct outcoming 3D structures for anisotropic etching of c-Si</li> <li>... are able to outline and define modern processes for microtechnology with specific focus on Si-based technology</li> <li>... are able to predict cost for simple microtechnology processes</li> </ul> <p><b>Analysis (4)</b></p> <ul style="list-style-type: none"> <li>... are able to deduce process a measurement errors from a given set of measurements</li> <li>... are able to compare deposition quality of different thin film techniques</li> <li>... are able to distinguish between etching techniques for 2D structuring and 3 D structuring</li> <li>... are able to distinguish different techniques in respect to quality measures e.g. critical dimension control</li> </ul> <p><b>Synthesis (5)</b></p> <ul style="list-style-type: none"> <li>... are able to summarize the most important technology steps within microfabrication</li> <li>... are able to prepare summarizing protocols of lab work</li> </ul>					

	<p><b>Learning Outcomes</b></p> <p><b>Evaluation (6)</b>                  ... are able to explain specific processes in microfabrication to lecturer and group members                  ... are able to choose proper parameters for specific processes in microfabrication</p>
<p><b>3</b></p>	<p><b>Individual Component Content</b></p> <p>a) I Introduction</p> <ul style="list-style-type: none"> <li>- Historical Trends in IC-technology and micromachining for MEMS/microsystems</li> </ul> <p>II Basic concepts in Si-technology</p> <ul style="list-style-type: none"> <li>- Deposition techniques (thin film technology), Doping and Diffusion, Lithography, Etching technology (wet and dry)</li> <li>- Cleanroom concepts, costs and yield issues in Si-technology</li> </ul> <p>III Micromachining for MEMS/microsystems</p> <ul style="list-style-type: none"> <li>- Bulk micromachining (including electrochemical etching of c-Si), surface micromachining, LIGA, Process integration aspects/ smart sensors</li> </ul> <p>IV New developments for micromedicine and application examples</p> <p>V Chemistry of Semiconductor Processes:</p> <ul style="list-style-type: none"> <li>- Materials and classifications</li> <li>- Hazardous Substances in Microelectronics</li> </ul> <p>Practical training:</p> <ul style="list-style-type: none"> <li>- Processes in cleanroom: Lithography, thin film deposition, Etching, Oxidation quality in microtechnology, packaging and bonding techniques</li> <li>- Process simulation with Suprem/Intellisuite</li> </ul>
<p><b>4</b></p>	<p><b>Teaching Methods</b></p> <p>a) Lecture / Practical</p>
<p><b>5</b></p>	<p><b>Prerequisites</b></p> <p>Physics, mathematics/calculus (differential equations, higher functions, integrals), material science of semiconductors</p>

<b>6</b>	<b>Methods of Assessment</b> a) Graded Assessment 1K (Written Exam) (6 LP insgesamt für alle Teilprüfungsleistung dieser Lehrveranstaltung) a) Non Graded Assessment 1sbL (Laboratory)
<b>7</b>	<b>Applicability of Module</b> Smart Systems M.Sc. (SMA)
<b>8</b>	<b>Person Responsible for Module</b> Prof. Dr. Ulrich Mescheder (Module Responsible)
<b>9</b>	<b>Reading List (Core Texts and Recommended Texts)</b> a) Campbell, S. A., Lewerenz, H. J.: Semiconductor micromachining, Vol. 2, Technologies and Industrial Applications, Wiley & Sons 1998 Sze, S. M. 1936-: VLSI technology, [internat. ed.], 2. ed., McGraw-Hill 1988 Schumicki, Guenter; Seegebrecht, Peter: Prozeßtechnologie : Fertigungsverfahren für integrierte MOS-Schaltungen, Springer 1991 Mescheder, Ulrich: Mikrosystemtechnik : Konzepte und Anwendungen; mit 23 Tab., 2. , überarb. u. erg. Aufl., unveränd. Nachdr., Teubner 2010 Globisch, Sabine: Lehrbuch Mikrotechnologie für Ausbildung, Studium und Weiterbildung, Hanser Verlag 2011 (E-Book)